

FIG. 5

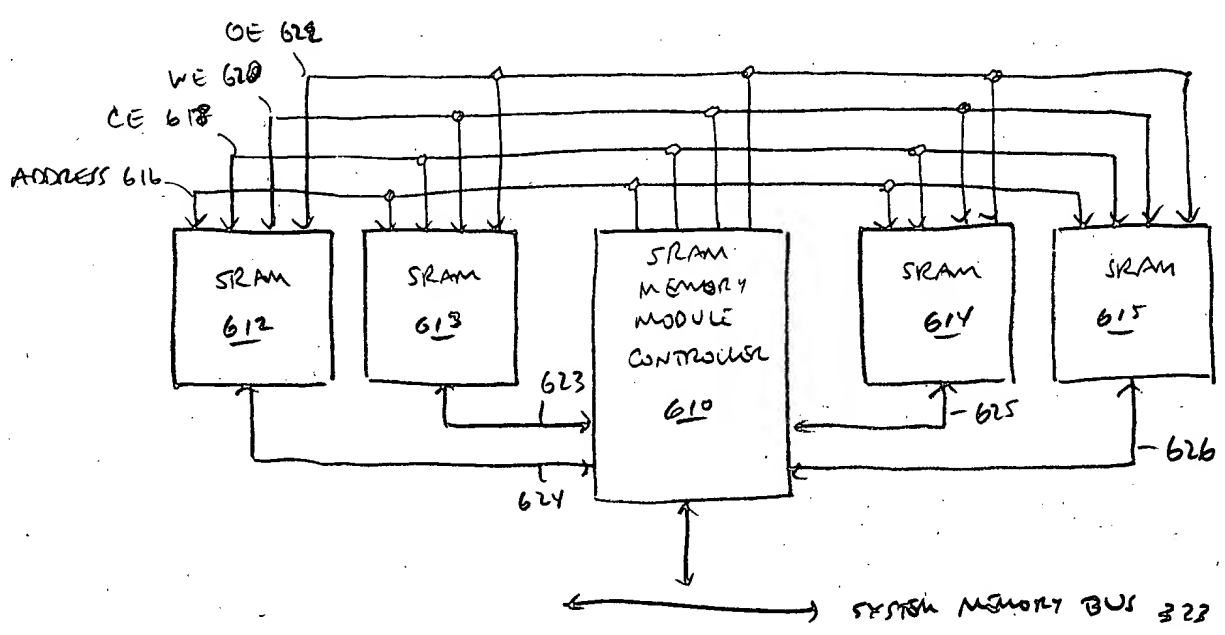


FIG. 6





700

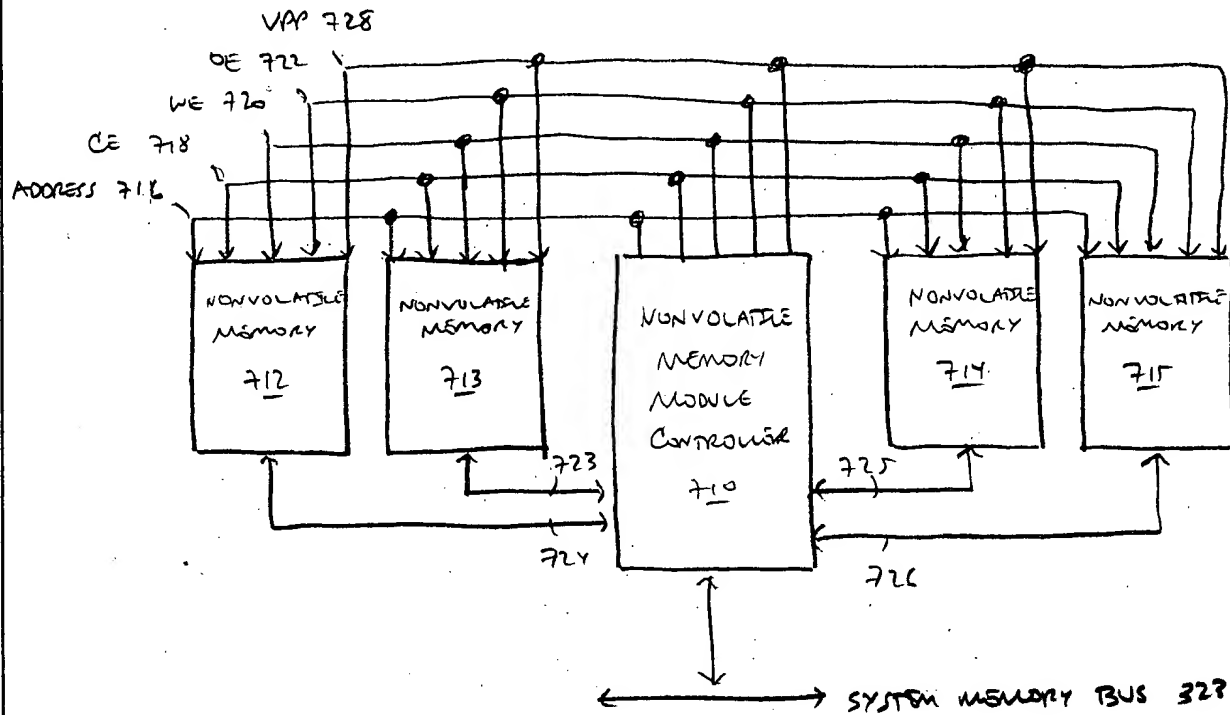


FIG. 7

to memory devices

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS



366T20-07E2060

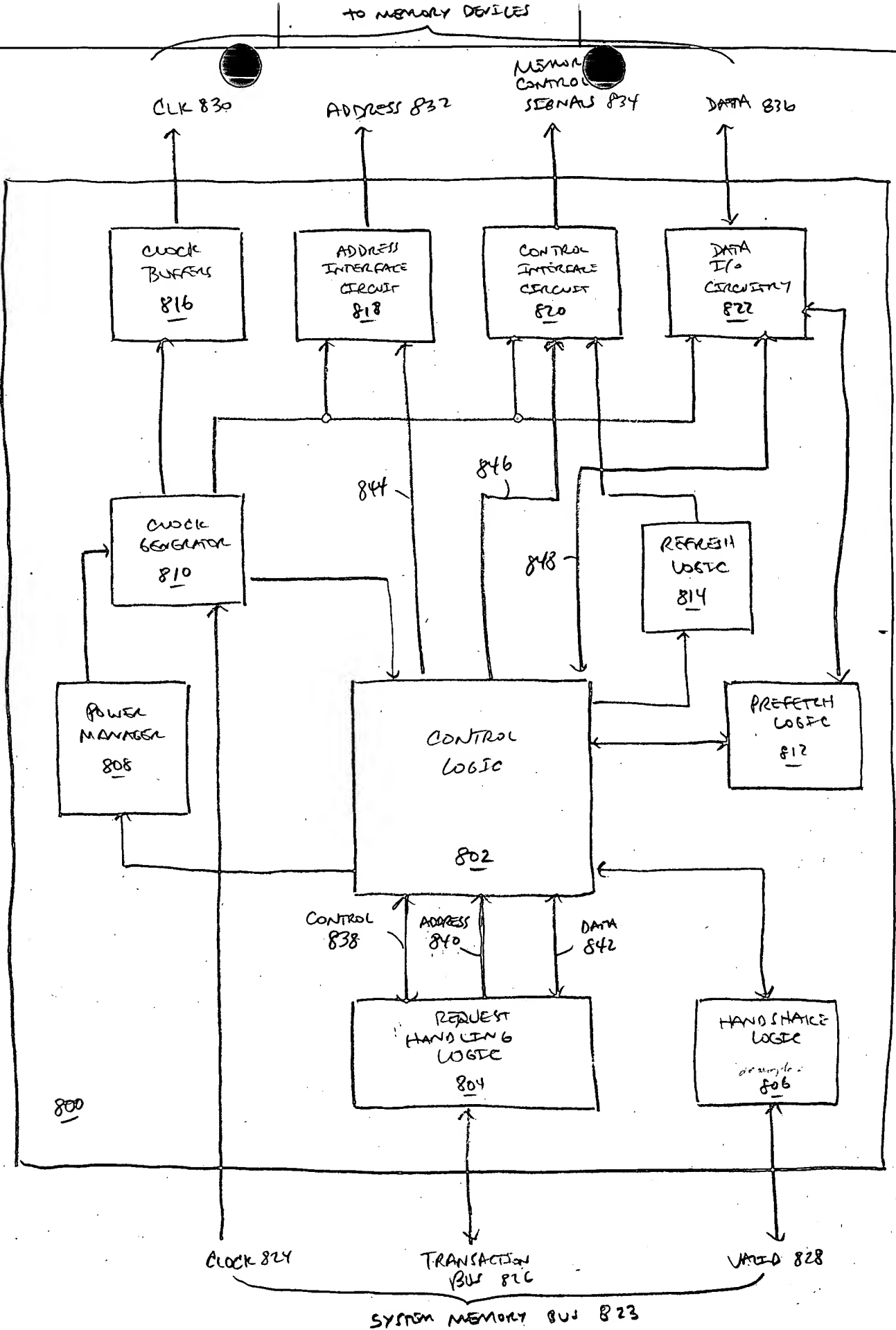
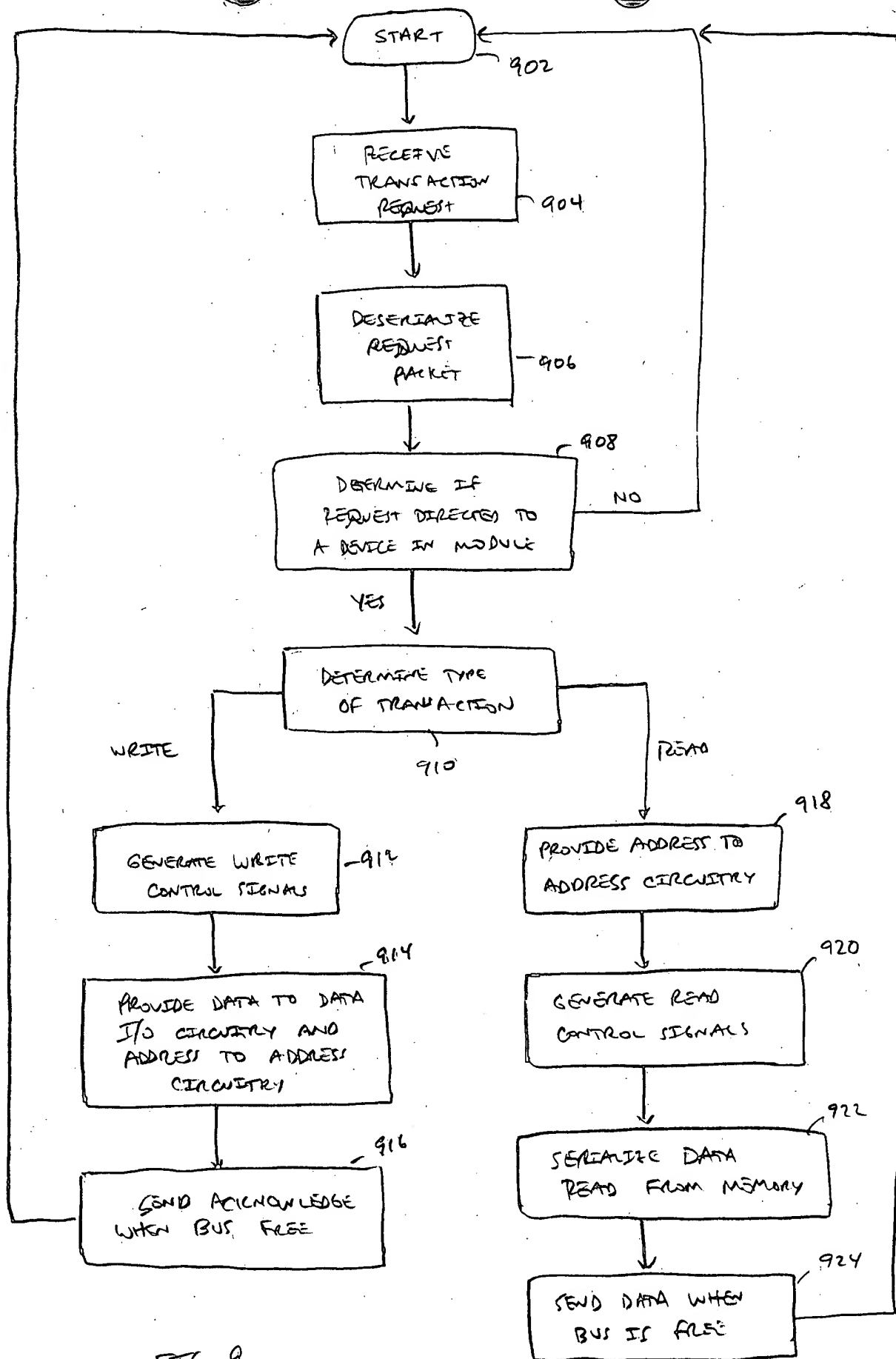


FIG. 8



TOP MEMORY DENSITIES



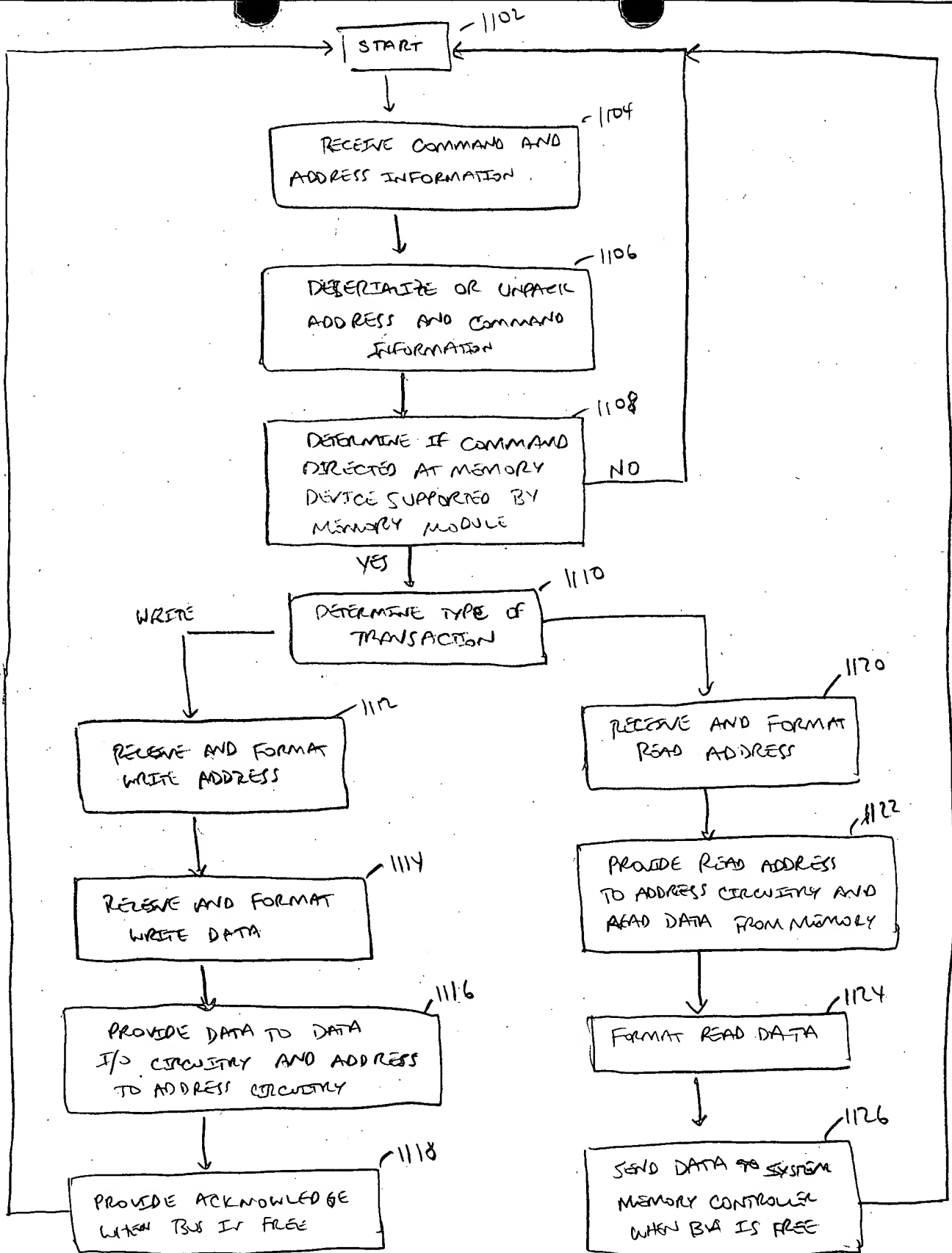


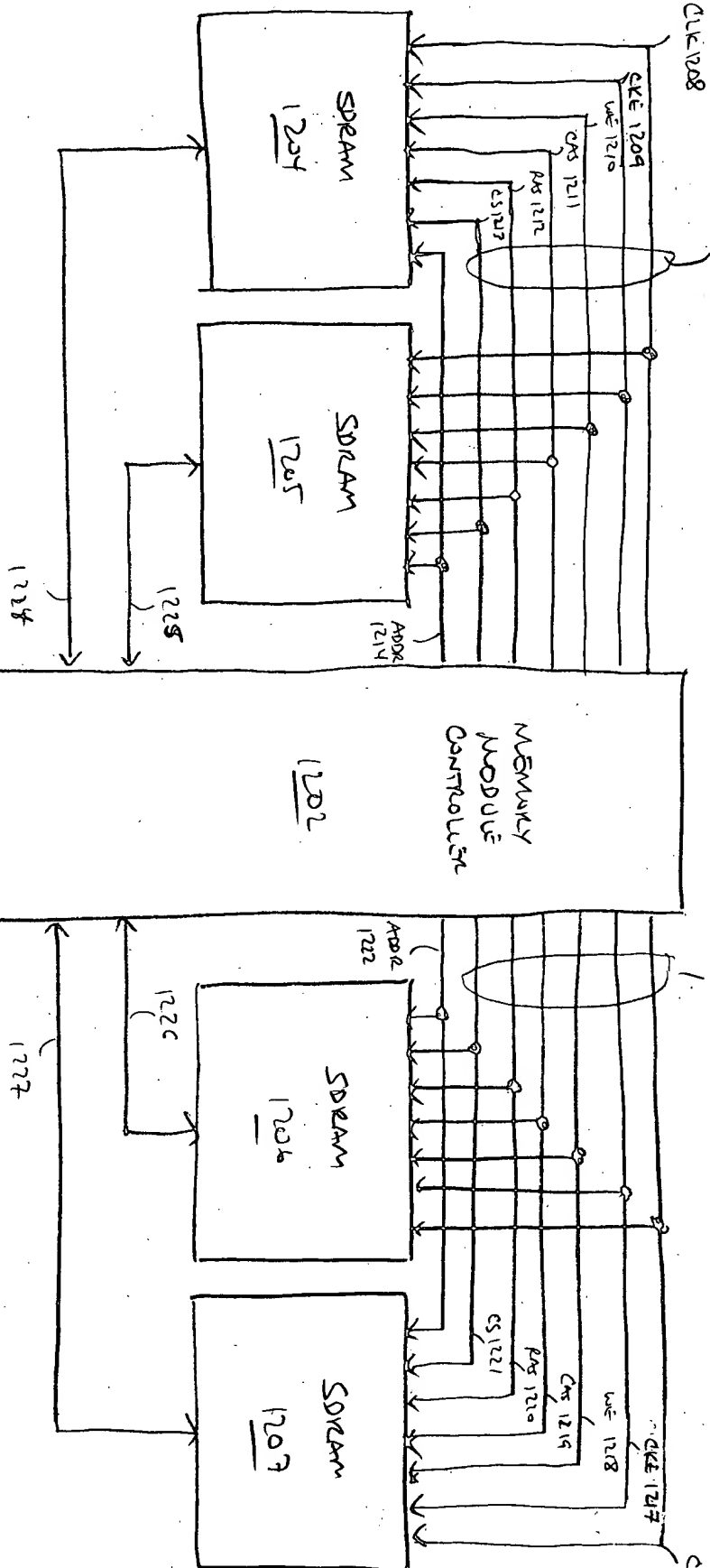
FIG. 11

Control Bus 1238

Control Bus 1240

CLK 1208

9121750



12. 12

SYSTEM MAIN ONLY BUS 1242



RECEIVED

50 SHEETS
100 SHEETS
200 SHEETS

22-141
22-142
22-144



SECRET - 04 FEB 2005

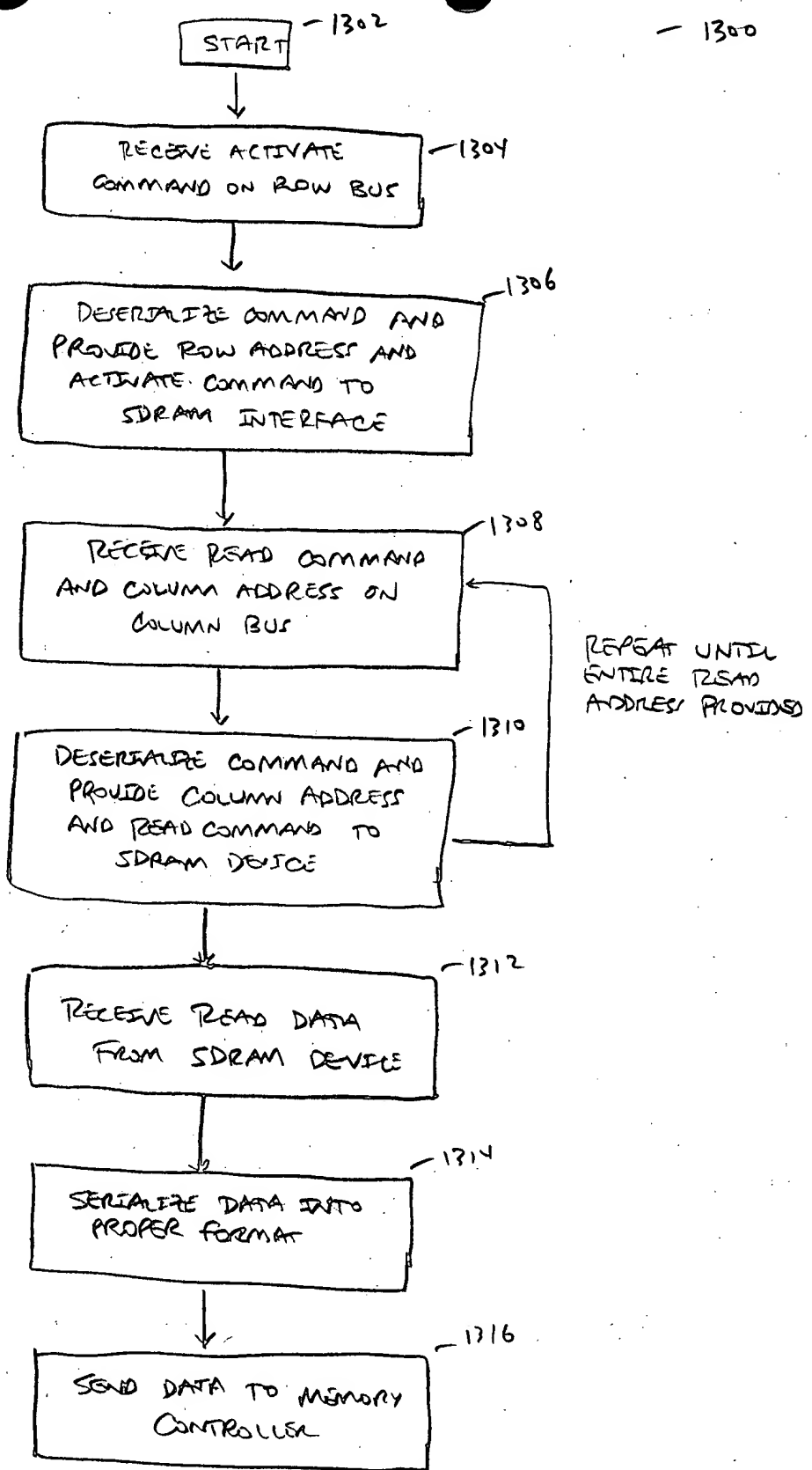


FIG. 13

CLK 1228



Row 1234



Column 1236



Data 1232



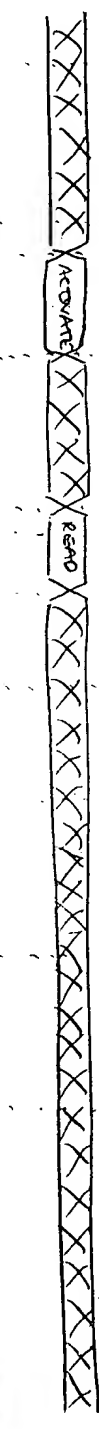
CLK 1208, 1216



ADDR 1214, 1222



Control Bus 1238, 1240



Data 1224-1227



t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10 t11 t12 t13 t14 t15

Fig. 14



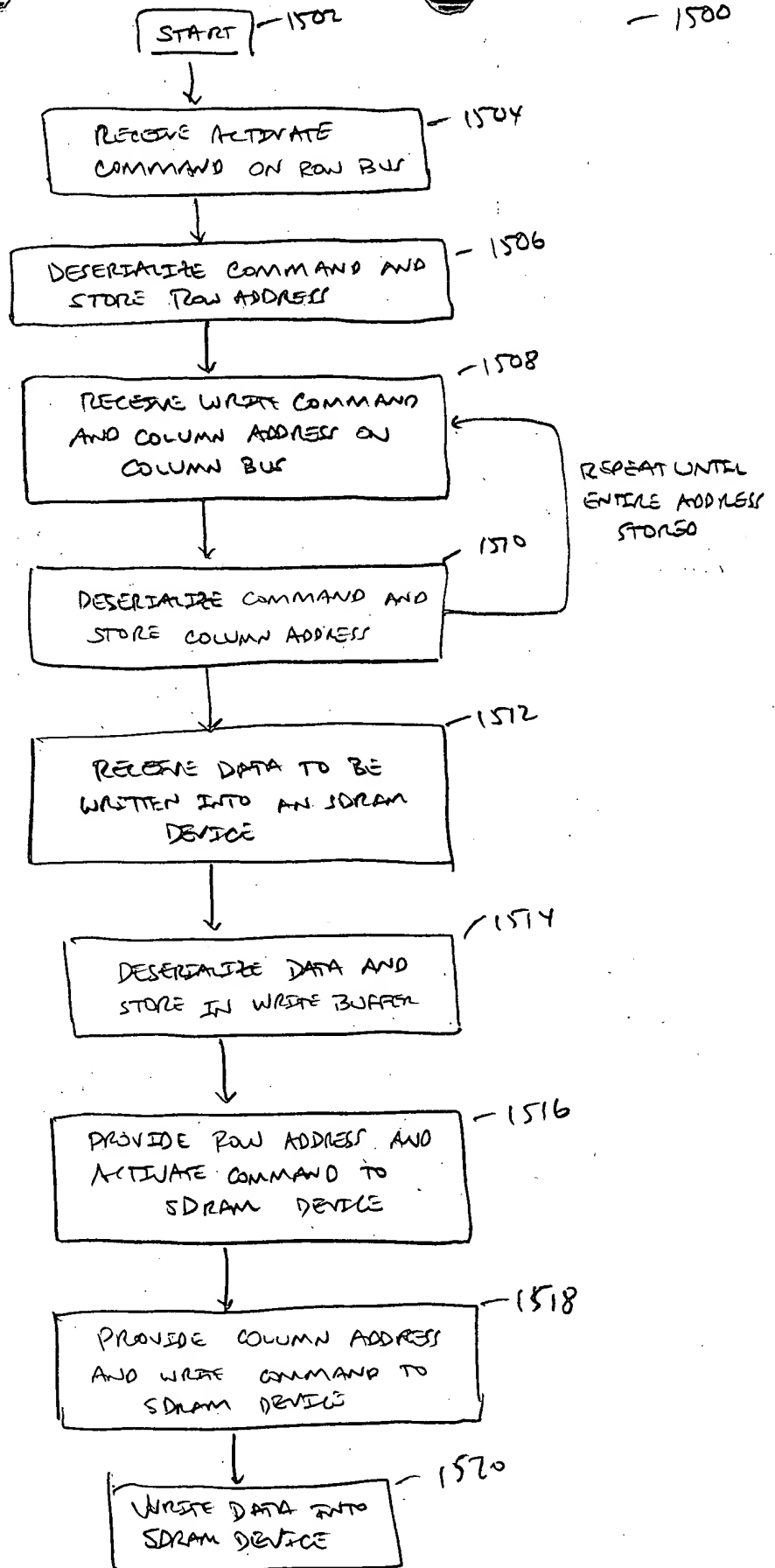
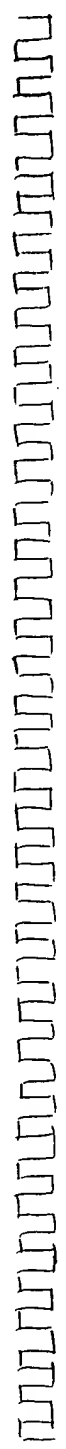


FIG. 15

CLK 1228



Row 1234



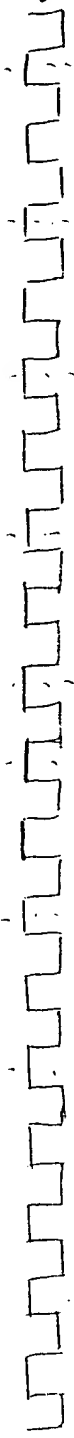
COLUMN 1236



DATA 1232



CLK 1208, 1216



ADDR 1214,
1222



CONTROL
BUS 1238, 1246



DATA 1224-
1227

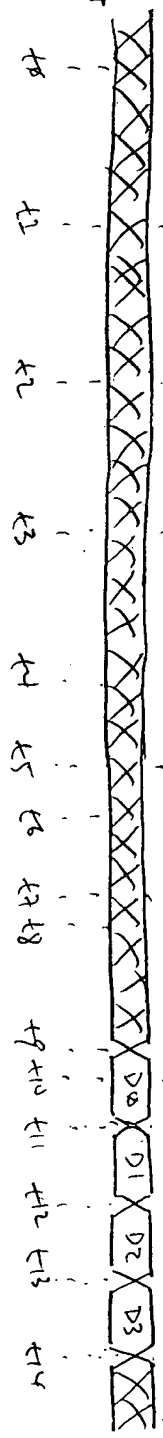
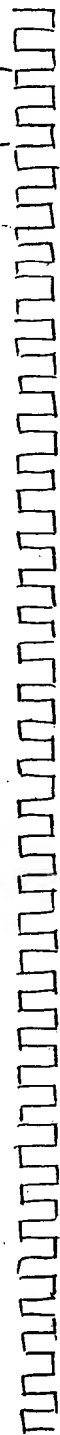


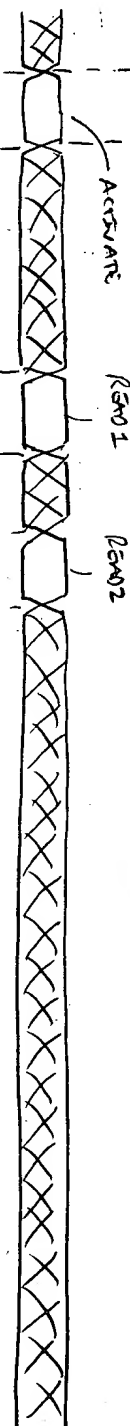
FIG. 16



CLK 1228



CONTROL BUS 1235



DATA 1232



CLK 1208, 1216



ADDR 1214, 1222



CONTROL BUS 1238, 1240



DATA 1214-1222

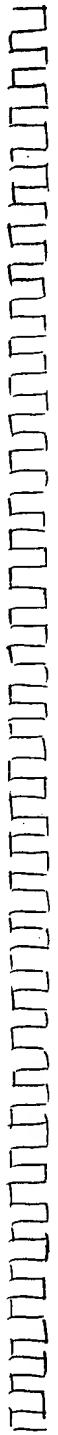


t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10 t11 t12 t13 t14 t15 t16 t17

Fig. 17



CLK 1228

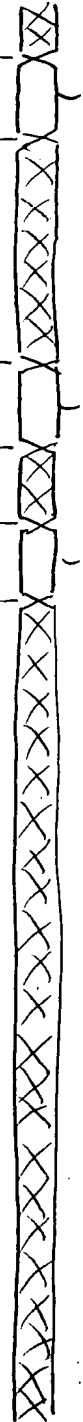


ACTIVE

WRITE 1

WRITE 2

CONTROL BUS 1235



DATA 1232



CLK 1208, 1218



ADDR 1214, 1222



CONTROL BUS 1238, 1240



DATA 1224-1227

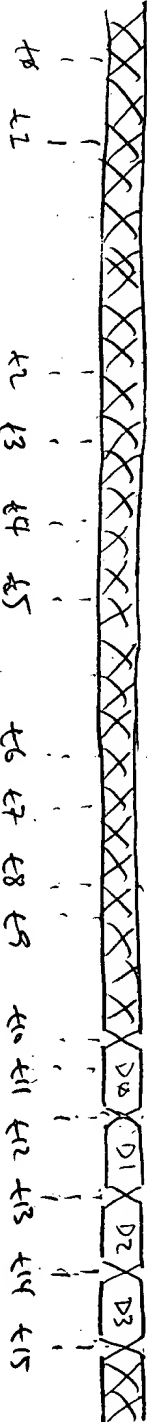


Fig. 18

